



M70A SPI NAND Continuous Read Default Off Datasheet Change

PCN: 33344

Published: 2019-08-12

Type: Datasheet Change

Description: M70A SPI NAND device functionality will change from Continuous Read Mode ON to OFF as default after power-on. Continuous Read Mode still can be Enabled and Disabled by sending the SET FEATURE command. All Continuous Read functionality will remain unchanged.

With Continuous Read Mode enabled the page size outputted is limited to the 4096 Byte User data area of the page. Now that continuous read is disabled by default the whole 4096+256 Byte will be accessible.

Reason: Better Align With Industry Standard

Product Affected: M70A SPI NAND

Affected Micron Part Number Component	Recommended Replacement	Customer Part Number
MT29F4G01ABAFD12-AAT:F		?
MT29F4G01ABAFD12-IT:F		?
MT29F4G01ABAFDWB-IT:F		?
MT29F4G01ABBFD12-AAT:F		?
MT29F4G01ABBFD12-IT:F		?
MT29F4G01ABBFDWB-IT:F		?
MT29F8G01ADAFD12-AAT:F		?
MT29F8G01ADAFD12-IT:F		?
MT29F8G01ADBFD12-AAT:F		?
MT29F8G01ADBFD12-IT:F		?
<u>Wafer</u>		
MT29F4G01ABAFDM70A3WC1		
MT29F4G01ABBFD70A3WC1		

*Materials that have been ordered are in **bold**.

Method of Identification: Data sheet will be updated

Micron Sites Affected: Fab 6 - US

Product Ship Date: 2019-11-10

NOTE: Per JEDEC Standard JESD46-C Section 3.2.3; lack of acknowledgment of this PCN within 30 days constitutes acceptance of change.

Micron Confidential and Proprietary Information

Attachments

There are no attachments on this PCN